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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/939,199	08/24/2001	Jeffrey J. Norris	2316.1485US01	3383

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MERCHANT & GOULD PC  
P.O. BOX 2903  
MINNEAPOLIS, MN 55402-0903

EXAMINER

LEON, EDWIN A

ART UNIT	PAPER NUMBER
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2833

DATE MAILED: 06/05/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application N .

09/939,199

Applicant(s)

NORRIS ET AL.

Examiner

Edwin A. León

Art Unit

2833

-- The MAILING DATE of this communication appears n th cov r sh et with th correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 5.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Claim Rejections - 35 USC § 112***

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claims 1-14 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claims 1 and 10, recite the limitation "at least one mount apparatus" in Line 5 and 3, respectively. It is unclear if the mount apparatus is one of the plurality of mount apparatuses recited in Line 2 or a different one. For examination purposes, the Examiner will assume that the mount apparatus is one of the plurality of mount apparatuses.

### ***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-8 and 10-14 are rejected under 35 U.S.C. 102(b) as being anticipated by Gladd et al. (U.S. Patent No. 6,000,952). With regard to Claim 1, Gladd et al.

discloses a system comprising: a chassis (Column 3, Lines 12-60) arranged and configured to retain a plurality of mount apparatuses (150), the chassis (Column 3, Lines 12-60) including a power bus (102) having a plurality of power plugs (upper part of 152, Fig. 5) for providing electrical power; and at least one mount apparatus (150) mounted in the chassis (Column 3, Lines 12-60), the mount apparatus (150) including a power receptacle (120) for receiving electrical power from one of the plurality of power plugs (upper part of 152, Fig. 5). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 2, Gladd et al. discloses the power bus (102) further including a power intake (122) for receiving electrical power. See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 3, Gladd et al. discloses the chassis (Column 3, Lines 12-60) further including first and second cable guides (120). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 4, Gladd et al. discloses a jack assembly having electrical contacts (126) that are electrically connected to the mount apparatus (150). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 5, Gladd et al. discloses the chassis (Column 3, Lines 12-60) including slots (124) for retaining the jack assembly. See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 6, Gladd et al. discloses the mount apparatus (150) including: a front cover (Fig. 4, upper 152) having a plurality of receptacles (120); a back cover (Fig. 4, lower 152) having a plurality of through holes (where contacts 126 are

located); and a circuit board assembly (112) sandwiched between the front cover (Fig. 4, upper 152) and the back cover (Fig. 4, lower 152). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 7, Gladd et al. discloses the circuit board assembly (112) including a board having a plurality of through holes (where contacts 126 are located) aligned with the receptacles (120) of the front cover (Fig. 4, upper 152) and the through holes (where contacts 126 are located) of the back cover (Fig. 4, lower 152); a plurality of contacts (126) retained in a first set of the through holes (where contacts 126 are located) of the board of the circuit board assembly (112), a first end of each contact (126) being extended towards and exposed in a corresponding receptacle of the front cover (Fig. 4, upper 152) and stopped by the front cover (Fig. 4, upper 152), a second end of each contact (126) being extended towards and projecting toward the back cover (Fig. 4, lower 152); a plurality of pins (126) retained in a second set of the through holes (where contacts 126 are located) of the board of the circuit board assembly (112), a first end of each pin being extended towards and stopped by the front cover (Fig. 4, upper 152), a second end of each pin being extended towards and projected from a corresponding through hole of the back cover (Fig. 4, lower 152); and a trace electrically connecting each contact (126) to each corresponding pin. See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 8, Gladd et al. discloses the mount apparatus (150) including a circuit board assembly (112) having a plurality of electrical terminals. See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 10, Gladd et al. discloses a system comprising: a chassis (Column 3, Lines 12-60) arranged and configured to retain a plurality of mount apparatuses (150), at least one mount apparatus (150) mounted in the chassis (Column 3, Lines 12-60), the mount apparatus (150) including: a front cover (Fig. 4, upper 152) having a plurality of receptacles (120); a back cover (Fig. 4, lower 152) having a plurality of through holes (where contacts 126 are located); and a circuit board assembly (112) sandwiched between the front cover (Fig. 4, upper 152) and the back cover (Fig. 4, lower 152). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 11, Gladd et al. discloses the circuit board assembly (112) including: a board having a plurality of through holes (where contacts 126 are located) aligned with the receptacles (120) of the front cover (Fig. 4, upper 152) and the through holes (where contacts 126 are located) of the back cover (Fig. 4, lower 152); a plurality of contacts (126) retained in a first set of the through holes (where contacts 126 are located) of the board of the circuit board assembly (112), a first end of each contact (126) being extended towards and exposed in a corresponding receptacle of the front cover (Fig. 4, upper 152) and stopped by the front cover (Fig. 4, upper 152), a second end of each contact (126) being extended towards and projecting toward the back cover (Fig. 4, lower 152); a plurality of pins (126) retained in a second set of the through holes (where contacts 126 are located) of the board of the circuit board assembly (112), a first end of each pin (126) being extended towards and stopped by the front cover (Fig. 4, upper 152), a second end of each pin (126) being extended towards and projected from a corresponding through hole of the back cover (Fig. 4, lower 152); and a trace (122)

electrically connecting each contact (126) to each corresponding pin (126). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 12, Gladd et al. discloses the chassis (Column 3, Lines 12-60) further including first and second cable guides (120). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 13, Gladd et al. discloses a jack assembly having electrical contacts (126) that are electrically connected to the mount apparatus (150). See Fig. 4 and Column 3, Lines 12-60.

With regard to Claim 14, Gladd et al. discloses the chassis (Column 3, Lines 12-60) including slots (130) for retaining the jack assembly. See Fig. 4 and Column 3, Lines 12-60.

### ***Claim Rejections - 35 USC § 103***

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Gladd et al. (U.S. Patent No. 6,000,952) in view of Jayne (U.S. Patent No. 3,682,792). Gladd et al. discloses the claimed invention except for the electrical terminal including: a first section that receives an electrical contact, the first section including first and second

spring arms proximate to each other at a contact point and configured to exert a first spring force to retain the electrical contact; a second section adapted for insertion into the through hole of the circuit board, the second section including first and second pin members proximate to each other and defining first and second slots configured to exert a second spring force to retain the electrical terminal in the through hole of the circuit board, the second spring force being exerted in a direction perpendicular to the first spring force; and a third section integral with the first and second sections.

Jayne discloses an assembly having an electrical terminal (10) including: a first section (12) that receives an electrical contact, the first section (12) including first and second spring arms (12', 12'') proximate to each other at a contact point and configured to exert a first spring force to retain the electrical contact; a second section (13) adapted for insertion into a through hole (29) of a circuit board (27), the second section (13) including first and second pin members (21) proximate to each other and defining first and second slots (spaces between 21) configured to exert a second spring force to retain the electrical terminal (10) in the through hole (29) of the circuit board (27), the second spring force being exerted in a direction perpendicular to the first spring force; and a third section (15) integral with the first (12) and second (13) sections. See Fig. 1.

Thus, it would have been obvious to one with ordinary skill in the art to modify the system of Gladd et al. by including a electrical terminal including: a first section that receives an electrical contact, the first section including first and second spring arms proximate to each other at a contact point and configured to exert a first spring force to retain the electrical contact; a second section adapted for insertion into the through hole



of the circuit board, the second section including first and second pin members proximate to each other and defining first and second slots configured to exert a second spring force to retain the electrical terminal in the through hole of the circuit board, the second spring force being exerted in a direction perpendicular to the first spring force; and a third section integral with the first and second sections as taught in Jayne to increase the pressure against an inserted contact member.

### ***Conclusion***

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Brussalis et al. (U.S. Patent No. 5,715,135), Miniet (U.S. Patent No. 4,533,188), Chiang (U.S. Patent No. 5,816,829), Wilson (U.S. Reissue Patent No. 35,231), Wrabel et al. (U.S. Patent No. 3,668,476), Miller, Jr (U.S. Patent No. 5,155,905), Daly et al. (U.S. Patent No. 5,967,802) and Paladel (U.S. Patent No. 5,064,388) disclose systems having mount apparatuses and power receptacle (120)s.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin A. León whose telephone number is (703) 308-6253. The examiner can normally be reached on Monday - Friday 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on (703) 308-2319. The fax phone numbers for the organization where this application or proceeding is assigned are (703)

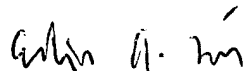
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308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

  
Edwin A. León  
AU 2833

  
P. AUSTIN BRADLEY  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2800

EAL  
May 29, 2002